

## Refine Search

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Term	Documents
(28 AND 31).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	64
(L31 AND L28).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	64

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### Search History

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side by side			
DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR			
<u>L33</u>	L31 and l28	64	<u>L33</u>
<u>L32</u>	L31 and l26	91	<u>L32</u>
<u>L31</u>	l25 and register\$1 near4 (bank\$1 or file\$1 or group\$5)	145	<u>L31</u>
DB=PGPB,USPT; PLUR=YES; OP=OR			
<u>L30</u>	l25 and l28	136	<u>L30</u>
<u>L29</u>	l25 and l26	183	<u>L29</u>
<u>L28</u>	(712/239,240)[CCLS]	663	<u>L28</u>
<u>L27</u>	(712/230-245)![CCLS]	3082	<u>L27</u>
<u>L26</u>	(712/230-245)[CCLS]	3082	<u>L26</u>
DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR			
<u>L25</u>	L24 near55 (updat\$5 or modif\$6)	264	<u>L25</u>

<u>L24</u>	branch\$5 near12 predict\$5 near15 (status or condition\$4 or bit\$1)	2331	<u>L24</u>
<u>L23</u>	L20 and l8	18	<u>L23</u>
<u>L22</u>	L20 and l7	180	<u>L22</u>
<u>L21</u>	L20 and l6	405	<u>L21</u>
<u>L20</u>	l5 and register\$1 near4 (bank\$1 or file\$1 or group\$5) and opcod\$3	571	<u>L20</u>
<u>L19</u>	L18 and l6	629	<u>L19</u>
<u>L18</u>	l5 and register\$1 near4 (bank\$1 or file\$1 or group\$5)	933	<u>L18</u>
<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>			
<u>L17</u>	l5 and l8	34	<u>L17</u>
<u>L16</u>	l5 and l7	300	<u>L16</u>
<u>L15</u>	l5 and l6	732	<u>L15</u>
<u>L14</u>	l4 and l8	35	<u>L14</u>
<u>L13</u>	l4 and l7	374	<u>L13</u>
<u>L12</u>	l4 and l6	863	<u>L12</u>
<u>L11</u>	l3 and l8	43	<u>L11</u>
<u>L10</u>	l3 and l7	521	<u>L10</u>
<u>L9</u>	l3 and l6	1286	<u>L9</u>
<u>L8</u>	(711/212-214)[CCLS]	791	<u>L8</u>
<u>L7</u>	(712/201-213)[CCLS]	2668	<u>L7</u>
<u>L6</u>	(712/2-300)[CCLS]	12675	<u>L6</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L5</u>	L4 and (convert\$4 or translat\$4 or conversion\$1)	1122	<u>L5</u>
<u>L4</u>	L3 and operand\$1 near12 address\$5	1302	<u>L4</u>
<u>L3</u>	L2 and instruction near4 (format\$3 or field\$1 or portion\$1 or byte\$1)	2640	<u>L3</u>
<u>L2</u>	(size or width or length) near12 decod\$3 and (parallel\$6 or concurrent\$3 or simultaneous\$4)	20427	<u>L2</u>
<u>L1</u>	(size or width or length) and (parallel\$6 or concurrent\$3 or simultaneous\$4)	2946524	<u>L1</u>

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Citation



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IEEE JNL IEEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEEE CNF IEEE Conference Proceeding

IEEE STD IEEE Standard

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Sager, D.; Hinton, G.; Upton, M.; Chappell, T.; Fletcher, T.D.; Samaan, S.; Murray, R.;

[Solid-State Circuits Conference, 2001. Digest of Technical Papers. ISSCC, 2001 IEEE International](#)  
5-7 Feb. 2001 Page(s):324 - 325, 461

Digital Object Identifier 10.1109/ISSCC.2001.912658

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